

Workshop

FPGA-2010 Pre-Conference Workshop on Open-Source for FPGA

Workshop Co-Chairs: Shep Siegel, Atomic Rules and Mike Wirthlin, BYU

To be held on Sunday February 21 starting at 2 PM
Monterey Beach Resort, Monterey, CA (Conference Location)

“Open Source”, ubiquitous in the software community, has grown to become vital in application domains served by reconfigurable computing. But what exactly is “Open Source”, and what are the values and pitfalls it brings? This workshop draws together technologists from academia and industry to share their experiences, opinions, and lessons learned.

Workshop Presentations:

- **“Open Source Hardware: A Bad Idea?” – Derek Chiou, University of Texas**
- **“The Open Core Protocol (OCP-IP)” – Ian Mackintosh, OCP-IP**
- **“The USRP Family of Open Source Software Defined Radios” – Matt Ettus, Ettus Research**
- **“Open Hardware Repository (OHR)” – Javier Serrano, CERN**
- **“OpenCPI, the Component Portability Infrastructure” – Jim Kulp, Mercury Federal Systems**
- **“Experience with the NetFPGA Program” – John Lockwood, Algo-Logic Systems**
- **“VPR Introspective” – Jonathan Rose, University of Toronto**

Categories and Subject Descriptors:

K.5 [Legal Aspects of Computing]; K.6 [Management of Computing and Information Systems]; A.1 [Introductory and Survey]

General Terms:

Open-Source, FPGA, Middleware, Interface, Implementation

Keywords:

Field-Programmable Gate Array. FPGA, applications, Open-Source

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